




SETHU INSTITUTE OF TECHNOLOGY

(An Autonomous Institution| Accredited with 'A++' Grade by NAAC)

Pulloor, Kariapatti –Taluk. Virudhunagar Dist-626115.



Department of Electronics and Communication Engineering					
Name	S.JANARTHANAN				
Date of Birth	05-10-1983				
Unique ID	1-456211366				
Educational Qualifications	M.E				
Designation	Assistant Professor				
Email ID	janarthanan@sethu.ac.in				
Alternate Email ID	janarthanans83@gmail.com				
Experience	Industry	Teaching	Others	Total	
	3.6	14.6	-	18	
Date of Joining the Institution	29.06.2022				
Area of Specialization	Antenna and Communication				
Courses taught	Antenna and Wave Propagation, Digital Signal Processing, Digital Electronics and Design , Electron Devices, Signals & Systems, Control System, Circuit Theory, VLSI Design.				
Research Focus	Antenna				
Research guidance (Number of Scholars)					
Subject Competency	VLSI Design, DSP, Signals and Systems, Digital Electronics				
No. of papers published	National Journals	International Journals		Conferences	
	1	1		3	
PG Specialization	VLSI Design				
Ph.D. Specialization	Antenna design				
Projects Carried out					
Patents (Filed & Granted)	4				



Tel: 04566304600
Web: www.sethu.ac.in

Email: sit@sethu.ac.in

Academic Credentials

Level	Degree	Specialization	University	Year of Completion
UG	BE	ECE	Anna university	2008
PG	ME	ME VLSI Design	Anna University	2015
Ph.D.	Ph.d (Doing)	Antenna	Vel Tech Rangarajan Dr. Sagunthala R&D Institute of Science and technology, Chennai,	Doing

Details of Journal Publication:

S Janarthanan, 2017, 'Implementation of IOT in smart irrigation system using arduino processor' , International Journal of Civil Engineering and Technology, vol. 8, issue.10, pp. 1304-1314 (**Scopus indexed**)

S Janarthanan , 2025 Advanced Miniaturized Microstrip Patch Antenna Design for High-Efficiency 5G Applications. Measurement Science Review vol. 24, issue.6

Details of Conference attended:

- JANARTHANAN S**, 'Design of Approximate Adder in QCA International Conference on Emerging Trends in engineering & Technology ICETET 2015.
- JANARTHANAN S**, 2021, 'Approximate arithmetic design of adders in QCA', AICTE Sponsored Fourth National Conference on Knowledge based Inventive Electronics and Telecommunication Systems (NCKIETS-2021) Organized by KKR and KSR Institute of Technology and Sciences, Vinjanampadu, Guntur, A.P, INDIA, During 25-27 August 2021, Conference proceedings has been published with ISBN no – 978-81-951436-2-7.
- JANARTHANAN S**, 'Social Transformation through resource advance in electrical Engineering on 21st March 2014 in Sethu Institute of technology Madurai.

Details of Book Chapter and Books Published:

S Janarthanan 2023 A novel efficient AI-based EEG workload assessment system using ANN-DL algorithm., Springer Nature Singapore.

Details of Patents Filed and Granted:

Level	Application No	Title of the Patent	Status	Date of Status
International	202341034385 A	A SAFETY PROTECTION DEVICE FOR PREGNANT WOMEN	Published	30-06- 2023
National	445038-001	AI BASED DEVICE FOR BRAIN ABNORMALITY DETECTION	Granted	21-01- 2025
National	202441084774	ANTENNA DIVERSITY TECHNIQUES FOR IMPROVED RELIABILITY	Published	15-11- 2024